AMENDMENTS TO THE SPECIFICATION:

The specification has been amended at pages 6, 7 and 14 per the Examiner's objections, as follows:

Page 6, paragraph 5 that continues on page 7, REPLACE as follows:

The destination 112 receives the data packets 106 in a packet processor 122. The data packets 106 are provided to a buffer queue 124 having a maximum capacity of Qmax. A threshold circuit compares the occupancy of the queue 124 with thresholds T1 and T2 specified by the present invention. The threshold circuit 130 periodically provides an updated transmit rate to a transmit rate register 119 via connection 131. The threshold circuit 130 calculates and sets a lower

Page 7, paragraph one, REPLACE as follows:

The threshold T1 and an upper threshold T2 in the buffer 124 based upon Max value ~~132~~ 134, Qmax value ~~134~~ 132. The details of calculating the thresholds T1 and T2 are based on queuing analysis which will be provided hereinafter. The thresholds T1 and T2 are the thresholds in the buffer 124 used to prevent underflow and overflow, respectively. While two thresholds are described, any number of thresholds may be calculated for the buffer as will be described hereinafter. The transmit rate stored in the register 119 is then periodically communicated from a transmit rate unit 136 to the sender 102 via a communications link 142.

Page 14, paragraph five, REPLACE as follows:

Thresholds T1 and T2 are computed at initialization time from Qmax in a threshold circuit ~~132~~ 130 by means of three equations described herein, namely,

$T1 > Qmax/8$ Eq (a)

$T2 < 15*Qmax/16$ Eq (b)

$T1 <= T2- Qmax/16$ Eq(c).

Entry of the changes to the specification and withdrawal of the rejection under 35 USC 112/2 are requested.